

PROGRAMMABLE LOGIC DEVICE HAVING
EMBEDDED DUAL-PORT RANDOM ACCESS MEMORY
CONFIGURABLE AS SINGLE-PORT MEMORY

Abstract of the Disclosure

5 A programmable logic device has embedded
random access memory ("RAM") that can function equally
well in either single-port or dual-port operation. The
RAM is dual-port RAM whose read address inputs and
write address inputs are both connected to a conductor
10 bus via two different sparsely populated programmable
interconnection resources. The programmable
interconnection resources are arranged so that each
pair of corresponding read address and write address
inputs can be connected to at least one conductor in
15 common on the conductor bus, allowing the RAM to be
configured to mimic a single-port RAM as read address
signals and write address signals originating at remote
components of the programmable logic device "think"
they are being directed to the same address inputs.

/